

# IP Included in the Mainstream Package

Cortex Processors	Mali Multimedia Processing	CoreLink Interconnect	Corstone IP	CoreLink System Controllers
<ul style="list-style-type: none"> <li>Cortex-A53*</li> <li>Cortex-A35*</li> <li>Cortex-A34*</li> <li>Cortex-A32*</li> <li>Cortex-A7</li> <li>Cortex-A5 (UP and MP)</li> <li>Cortex-R52</li> <li>Cortex-R8</li> <li>Cortex-R5</li> <li>Cortex-M55</li> <li>Cortex-M33</li> <li>Cortex-M23</li> <li>Cortex-M7</li> <li>Cortex-M4</li> <li>Cortex-M3</li> <li>Cortex-M0+</li> <li>Cortex-M0</li> </ul>	<ul style="list-style-type: none"> <li>Mali-G52</li> <li>Mali-G31</li> <li>Frame Buffer Compression</li> <li>Mali-C52 ISP</li> <li>Mali-C32 ISP</li> </ul>	<ul style="list-style-type: none"> <li>CoreLink NIC-450</li> <li>CoreLink NIC-400</li> <li>CoreLink CCI-400</li> <li>CoreLink CCI-500</li> <li>CoreLink CCI-550</li> <li>CoreLink DPE-400</li> <li>CoreLink PCK-600</li> <li>CoreLink SIE-300</li> <li>ADB-400 AMBA domain bridge</li> <li>XHB-400 AXI-AHB</li> <li>XHB-500 AXI5-AHB5</li> </ul>	<ul style="list-style-type: none"> <li>Corstone-101</li> <li>Corstone-102</li> <li>Corstone-201</li> <li>Corstone-300</li> <li>Corstone-500</li> <li>Corstone-700</li> <li>Corstone-1000</li> <li>Corstone-1000 Crypto Ext.</li> </ul>	<ul style="list-style-type: none"> <li>CoreLink GIC-500</li> <li>CoreLink GIC-400</li> <li>PL192 VIC</li> <li>CoreLink TZC-400</li> <li>CoreLink L2C-310</li> <li>CoreLink MMU-500</li> <li>CoreLink SIE-200</li> <li>AHB System IP</li> <li>BP140 Mem. Intf</li> <li>BP141 TrustZone MW</li> <li>AHB Cache</li> <li>Corelink DMA-350</li> </ul>
	<h3>Safety Packages</h3> <ul style="list-style-type: none"> <li>Cortex-A53</li> <li>Cortex-A35, A34, A32</li> <li>Cortex-R52, R5</li> <li>Cortex-M33, M23</li> <li>Cortex-M55</li> <li>Cortex-M7, M4, M3</li> <li>Cortex-M0+</li> </ul>		<h3>CoreSight Debug and Trace</h3> <ul style="list-style-type: none"> <li>CoreSight SoC-600</li> <li>CoreSight SoC-600M</li> <li>CoreSight SoC-400</li> <li>CoreSight SDC-600</li> <li>CoreSight ELA-600</li> <li>CoreSight ELA-500</li> <li>CoreSight STM-500</li> <li>CoreSight STM</li> <li>CoreSight TMC</li> </ul>	
		<h3>Peripheral Controllers</h3> <ul style="list-style-type: none"> <li>PL011 UART</li> <li>PL022 SPI</li> <li>PL031 RTC</li> <li>CoreLink DMA-230</li> <li>CoreLink DMA-330</li> </ul>	<h3>Artisan Physical IP**</h3> <ul style="list-style-type: none"> <li>Artisan Physical IP platform, TSMC 22UML</li> <li>Artisan PIK for Cortex-M33, TSMC 22UML</li> <li>Artisan PIK for Cortex-M55, TSMC 22UML</li> <li>Artisan PIK for Ethos-U55, TSMC 22UML</li> <li>Thousands of Physical IP Libraries included in the Artisan Physical IP- Free</li> </ul>	
<h3>Ethos Machine Learning Processors</h3> <ul style="list-style-type: none"> <li>Ethos-U65</li> <li>Ethos U55</li> </ul>				

<h3>Tools &amp; Models</h3> <ul style="list-style-type: none"> <li>Access the latest tools &amp; models in Success Kits to accelerate and improve development</li> </ul>
<h3>Support</h3> <ul style="list-style-type: none"> <li>Global technical support</li> <li>Training</li> </ul>

\*Crypto extensions of Cortex-A53, A35, A34 and A32 and CryptoCell products may require Arm's trade compliance team to obtain an export license before we can enable download. This process can take several weeks to complete so please send the request as early as possible.

\*\*Additional logic IP, standard cell, embedded memory compilers and interface IP across many foundry nodes also available without a license fee