

About Arm Flexible Access

Arm Flexible Access lowers the barriers to rapid innovation and opens the doors to leading technology with upfront access to a wide range of Arm IP, support, tools, and training.

For a single annual fee, you can evaluate and design solutions before committing to production, jumpstart the concept-to-compute journey, and join one of the world's largest, most prolific, and creative communities of technology leaders.

For more information, contact your Arm account manager today or visit <https://arm.com/products/flexible-access>

What is included in Arm Flexible Access?

	Entry	Standard
Access to entire mainstream product package, including CPUs, GPUs, Corstone foundation IP, system IP and security IP	Y	
Technical support from Arm expert engineers	Y	
Onboarding session with an Arm support engineer	Y	
Evaluation, exploration, and full design rights	Y	
Number of tape-outs per year	1	Unlimited
Number of online training seats for introductory training on a selection of mainstream IP products	5	25
Arm Development Studio seats (Arm DS Gold)	1	3
Arm configuration tool seats - Socrates	1	1
Arm fixed configuration system models seats (fast and cycle-accurate)	1	2
Arm Service Tokens (can be used for training, design reviews, and onsite support)	Purchased separately	15 Service Tokens (equivalent to \$30k in value) for quick and flexible access to help, when you need it. Can be used for 3 days of onsite training or put toward a design review from Arm's expert engineering team.*

How much does Arm Flexible Access cost?

	Entry	Standard
Access fee	\$75k per annum	\$200k per annum
License fees (due on project manufacture)	Calculated per project based on IP used	
Royalties	Calculated per project and paid per unit shipped	

How does Arm Flexible work?



Sign up

Contact us at the sign-up link below, and pay the annual fee for access and support.

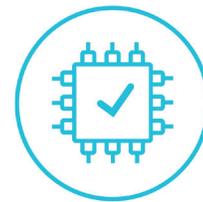
1



Download

Browse and select IP in our download hub. Experiment or fully design your product.

2



License

When ready for production, license with a simple order form and pay only for the IP used.

3

Product	Description	Learn more about this product
CPU Processing		
Cortex-A53 Processor	Low-power processor with 32-bit and 64-bit capabilities, applicable in a range of devices requiring high performance in power-constrained environments.	https://www.arm.com/products/silicon-ip-cpu/cortex-a/cortex-a53
Cortex-A35 Processor	Ultra-high efficiency smart device processor, the smallest and most power-efficient 32-bit and 64-bit Arm application processor.	https://www.arm.com/products/silicon-ip-cpu/cortex-a/cortex-a35
Cortex-A34 Processor	Low-power 64-bit only processor with ultra-high efficiency.	https://www.arm.com/products/silicon-ip-cpu/cortex-a/cortex-a34
Cortex-A32 Processor	Low-power 32-bit only processor with ultra-high efficiency.	https://www.arm.com/products/silicon-ip-cpu/cortex-a/cortex-a32
Cortex-A7 Processor	Power-efficient processor, designed for a wide range of devices with differing requirements demanding balance between power and performance.	https://www.arm.com/products/silicon-ip-cpu/cortex-a/cortex-a7
Cortex-A5 Multi-Processor	Smallest Cortex-A processor designed for applications that require virtual memory management for high-level operating systems within a low-power, low-area profile.	https://www.arm.com/products/silicon-ip-cpu/cortex-a/cortex-a5
Cortex-A5 Uni-Processor		https://www.arm.com/products/silicon-ip-cpu/cortex-a/cortex-a5
Cortex-R52 Processor	Designed for advanced silicon processes requiring high-performance and cost-effective processing. Delivers real-time performance for functional safety.	https://www.arm.com/products/silicon-ip-cpu/cortex-r/cortex-r52
Cortex-R8 Processor	Designed for products with high performance requirements where timing deadlines must always be met.	https://www.arm.com/products/silicon-ip-cpu/cortex-r/cortex-r8
Cortex-R5 Processor	Offers high-performance computing solutions for embedded systems that require reliability, high availability, fault tolerance, and real-time responses.	https://www.arm.com/products/silicon-ip-cpu/cortex-r/cortex-r5
Cortex-M33 Processor	Optimized for cost and power-sensitive microcontroller and mixed-signal applications. Designed for applications requiring efficient security or digital signal control. Includes Arm TrustZone security.	https://www.arm.com/products/silicon-ip-cpu/cortex-m/cortex-m33
Cortex-M23 Processor	Smallest and lowest-power microcontroller with Arm TrustZone security, ideal for applications requiring software isolation and security.	https://www.arm.com/products/silicon-ip-cpu/cortex-m/cortex-m23
Cortex-M7 Processor	The highest performance CPU in the energy-efficient Cortex-M processor family and includes digital signal processing (DSP) instructions.	https://www.arm.com/products/silicon-ip-cpu/cortex-m/cortex-m7
Cortex-M4 Processor	Designed to address applications requiring digital signal processing, with a blend of efficient, easy-to-use control and signal processing capabilities.	https://www.arm.com/products/silicon-ip-cpu/cortex-m/cortex-m4
Cortex-M3 Processor	Designed for cost-sensitive and power-constrained solutions in a broad range of devices. Balanced between area, performance, and power.	https://www.arm.com/products/silicon-ip-cpu/cortex-m/cortex-m3
Cortex-M0+ Processor	The smallest footprint and lowest power requirements of all Cortex-M processors, suitable for a wide variety of applications, including sensors and wearables.	https://www.arm.com/products/silicon-ip-cpu/cortex-m/cortex-m0-plus
Cortex-M0 Processor	Small footprint and high efficiency, ideal for simple, cost-sensitive devices.	https://www.arm.com/products/silicon-ip-cpu/cortex-m/cortex-m0

GPU Processing

Mali-G52 Graphics Processor	Designed to bring premium visual experiences to mainstream markets with heightened machine learning capabilities.	https://www.arm.com/products/silicon-ip-multimedia/gpu/mali-g52
Mali-G31 Graphics Processor	The first ultra-efficient GPU based on the Bifrost architecture targeting smaller applications such as IoT.	https://www.arm.com/products/silicon-ip-multimedia/gpu/mali-g31

Corelink Interconnect

CoreLink CCI-550 Cache Coherent Interconnect	Full coherency with up to six clusters including big.LITTLE and coherent accelerators. High performance and power efficiency with integrated snoop filter.	https://www.arm.com/products/silicon-ip-system/corelink-interconnect/cci-550
CoreLink CCI-500 Cache Coherent Interconnect	Full coherency with up to four clusters including big.LITTLE and coherent accelerators. High performance and power efficiency with integrated snoop filter.	https://www.arm.com/products/silicon-ip-system/corelink-interconnect/cci-500
CoreLink CCI-400 Cache Coherent Interconnect	Provides full cache coherency between two clusters of multi-core CPUs. Enables big.LITTLE processing and I/O coherency for devices.	https://www.arm.com/products/silicon-ip-system/corelink-interconnect/cci-400
CoreLink NIC-450 Network Interconnect	Highly configurable topology with network-on-chip properties for building high-performance, optimized, AMBA-compliant SoC connectivity, including QoS and Thin links.	https://www.arm.com/products/silicon-ip-system/corelink-interconnect/nic
CoreLink NIC-400 Network Interconnect	Highly configurable topology with network-on-chip properties for building high-performance, optimized, AMBA-compliant SoC connectivity.	https://www.arm.com/products/silicon-ip-system/corelink-interconnect/nic
CoreLink ADB-400 AMBA Domain Bridge	An asynchronous bridge between two components or systems that can be in a different power, clock, or voltage domains.	https://developer.arm.com/ip-products/system-ip/corelink-interconnect/corelink-network-interconnect-family
CoreLink XHB-400 AXI4-AHB Bridge	Converts AXI4 protocol to AHB-Lite protocol via an AXI4 slave interface and an AHB-Lite master interface.	https://developer.arm.com/ip-products/system-ip/corelink-interconnect/corelink-network-interconnect-family

System Controllers

CoreLink DMA-330 AXI DMA Controller	A high-performance DMA controller that can boost the performance and reduce the power consumption in AXI-based systems.	https://www.arm.com/products/silicon-ip-system/embedded-system-design/dma-330
CoreLink DMA-230 AHB Micro DMA Controller	Low gate count (3-10k gates) micro-DMA engine targeting AHB-based Cortex-M systems.	https://www.arm.com/products/silicon-ip-system/embedded-system-design/dma-230
CoreLink GIC-500 Generic Interrupt Controller	Detects, manages, virtualizes, and distributes interrupts for Armv8.0-A processors. Configurable up to 128 single-threaded cores and 960 shared interrupts.	https://www.arm.com/products/silicon-ip-system/system-controllers/gic
CoreLink GIC-400 Generic Interrupt Controller	Detects, manages, and virtualizes interrupts for Armv7 processors. Configurable up to 8 cores and 480 shared interrupts.	https://www.arm.com/products/silicon-ip-system/system-controllers/gic
PL192 Vectored Interrupt Controller	An advanced vectored interrupt controller supporting up to 32 vectored interrupts with programmable priority level and masking.	https://developer.arm.com/ip-products/system-ip/system-controllers/peripheral-controllers
CoreLink TZC-400 TrustZone Address Space Controller	Performs security checks on transactions to memory or peripherals, configurable up to 8 regions.	https://www.arm.com/products/silicon-ip-security/address-space-controllers
CoreLink L2C-310 AXI Level 2 Cache Controller	High-performance, AXI level 2 cache controller designed and optimized to address Arm AXI processors, normally used with Cortex-A5.	https://www.arm.com/products/silicon-ip-system/embedded-system-design/l2c-310

CoreLink MMU-500 System Memory Management Unit	System memory management unit that includes caching and memory virtualization. Enforces memory protection and access control, and is designed for use in a virtualized system where multiple guest operating systems are managed by a hypervisor. Supports Armv8-A and Armv7-A.	https://www.arm.com/products/silicon-ip-system/system-controllers/mmu
BP140 AXI Internal Memory Interface	AXI to on-chip SRAM interface.	https://developer.arm.com/docs/dto0009/a
BP141 TrustZone AXI Memory Interface	AXI to on-chip SRAM interface with support for Arm TrustZone protection for secure memory regions.	https://developer.arm.com/products/system-ip/system-controllers/other-system-controllers

Security IP

CryptoCell-312 Security IP	High-efficiency, low-footprint platform security and cryptographic services targeting multiple threats across various IoT domains.	https://www.arm.com/products/silicon-ip-security/crypto-cell-300
CryptoCell-712 Security IP	An outstanding level of security to target a broad set of threats, while addressing requirements for increased system complexity, high performance, low power consumption.	https://www.arm.com/products/silicon-ip-security/crypto-cell-700
True Random Number Generator	A mandatory component in any system that generates cryptographic assets.	https://www.arm.com/products/silicon-ip-security/random-number-generator

Peripheral Controllers

PL011 UART Universal Asynchronous Receiver/Transmitter	Peripheral controllers for UART, SPI and real-time clock.	https://developer.arm.com/ip-products/system-ip/system-controllers
PL022 SPI Synchronous Serial Port		https://developer.arm.com/ip-products/system-ip/system-controllers
PL031 RTC Real Time Clock		https://developer.arm.com/ip-products/system-ip/system-controllers

CoreSight Debug & Trace

CoreSight SoC-400 Debug and Trace	Configurable components, including debug access trace generation manipulation and output, cross triggering, and time stamping.	https://www.arm.com/products/silicon-ip-system/coresight-debug-trace/soc-400
CoreSight SDC-600 Secure Debug Channel	Addresses device security needs by allowing silicon and tool vendors to enforce protection and police debug access, and by working closely with cryptographic elements and debug certificate authentication.	https://www.arm.com/products/silicon-ip-system/coresight-debug-trace/sdc-600
CoreSight STM-500 System Trace Macrocell	Trace source for real-time software instrumentation with no impact on system behavior or performance. Extends the low-cost, real-time visibility of software and hardware execution to all software developers. Supports 64-bit memory interfaces.	https://www.arm.com/products/silicon-ip-system/coresight-debug-trace/coresight-stm-500
CoreSight System Trace Macrocell	System Trace Macrocell supporting 32-bit memory interfaces.	https://developer.arm.com/ip-products/system-ip/coresight-debug-and-trace/coresight-components/system-trace-macrocell
CoreSight Trace Memory Controller	A configurable trace component to terminate trace buses into buffers, FIFOs, or alternatively, to route trace data over AXI to memory or off-chip to interface controllers.	https://www.arm.com/products/silicon-ip-system/coresight-debug-trace/coresight-tmc

Physical IP

Artisan PIK for Cortex-M33 TSMC 22ULL	Processor Implementation Kit (PIK) for Cortex-M33 based on Artisan 22ULL Physical IP.	https://www.arm.com/products/silicon-ip-physical
Artisan Memory Compilers TSMC 22ULL	Physical IP solution for TSMC 22ULL, including memory compilers, logic libraries, GPIOs and documentation.	https://www.arm.com/products/silicon-ip-physical
Artisan SC7MC Logic Libraries TSMC 22ULL		
Artisan SC6MC Logic Libraries TSMC 22ULL		
Artisan GPIO TSMC 22ULL		
Artisan Documentation TSMC 22ULL		

Additional logic IP, standard cell, embedded memory compilers, interface IP and POP IP across many foundry nodes are also available without a license fee via DesignStart (designstart.arm.com).

Design Kits

Corstone-101 foundation IP	Contains a pre-integrated, verified subsystem and system IP that brings together all core elements for an SoC. Includes the CoreLink SSE-050 subsystem built around a Cortex-M3 processor. Other elements include CMSDK, AHB Flash Cache, RTC, TRNG, and a generic eFlash controller.	https://www.arm.com/products/iot/soc/corstone-101
Corstone-201 foundation IP	Incorporates the Arm SSE-200 subsystem for Cortex-M33 and the SSE-123 example subsystem built around the Cortex-M23. The subsystems provide a solid base for either mainstream or constrained device SoC design, with Arm TrustZone support for deep-rooted security.	https://www.arm.com/products/iot/soc/corstone-201

Virtual System Models

Virtual system models	<p>Fixed configuration systems for select CPUs within the mainstream package for benchmarking, performance analysis, and software development ahead of silicon.</p> <p>Fast and Cycle Model variants for each system. Software packages include prepackaged benchmarks and the ability to add own software.</p>	https://www.arm.com/products/development-tools/simulation/fixed-virtual-platforms
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