

May 26, 2010

Dear Customer:

Thank you for your purchase of *ARM Assembly: Fundamentals and Techniques*, by Willian Hohl.

The following are corrections by the author to the first printing of this book:

Chapter 1, page 16 – In the middle part of Example 1.6, the negative sign is missing from -124, so it should read:

```
-124  
+ 236
```

Chapter 5, page 67 – The fourth sentence should read “...if the base register r12 contained...” not register r9.

Chapter 5, page 67 – Example 5.2 is an example of a signed halfword read (not a byte), so the instruction should read as follows:

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LDRSH r11, [r0] ; load signed halfword into r11
```

Chapter 11, page 185 – The first sentence should read “Nearly all of the code in Example 11.2 (see pages 181–184) sets up...”

Chapter 13, page 213 – In the first paragraph on the page, the line “... the same operation can be done using only a 36-bit opcode...” should read “... only a 16-bit opcode...”.

Index, page 348 – The entry for STTR910FM32 should read STR910FM32.

We sincerely regret any inconvenience this may have caused you. Please let us know if we can be of any assistance regarding this title or any other titles that Taylor & Francis publishes.

Best regards,

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